Adaptive droop control design with overcurrent protection for onboard DC microgrids in hybrid electric aircrafts

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Conceptual schematic





Present and Future Trends





*Aerospace Technology Institute, INSIGHT_07: Electrical Power System.

Onboard DC Microgrids





Technical Challenges



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Aircraft subsystem to be upgraded	Architecture and interconnect	Energy generation and storage	Electrical machines	Power electronics
High-power generator				
Power sharing control				
Battery				
Fuel cell				
Propulsion motors (distributed)				
Power distribution & protection				
Overall system power demands				

Key: Red - major challenge; Blue - significant challenge; Grey - work required but risk manageable; White - N/A

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System modelling



Constant power loads (CPLs)

$$P = i_{CPL} V_{LV}$$
$$i_{CPL} = i_{PV} - i_{HV} + \sum_{i=1}^{n} g_i (V_i - V_{LV})$$

$$\begin{aligned} \forall i \in \{1, ..., n\} \\ C_i \frac{dV_i}{dt} &= (1 - u_i)i_{L,i} - i_i \\ L_{HV} \frac{di_{L,HV}}{dt} &= V_{LV} - r_{s,HV}i_{L,HV} - (1 - u_{HV})V_{HV} \\ C_{HV} \frac{dV_{HV}}{dt} &= (1 - u_{HV})i_{L,HV} - i_{HV} \end{aligned}$$





$$\tau_j \dot{V_j} = V_j^* - V_j - m_j i_j, \qquad \forall j \in \{1, \dots, n, HV\}$$

Drawbacks:

- Mismatches in output/line resistances cause inaccurate power sharing.
- Trade-off between the voltage regulation and load power sharing.
- External protection circuits/limiters are required under faulty conditions.

Proposed robust droop for converters interfacing BESS (HV interconnection) takes the form.

$$\tau_{i}\dot{V}_{i} = V^{*} - V_{LV} - \frac{m_{i}}{SOC_{i}^{\rho}}i_{i}, \qquad \forall i \in \{1, ..., n\} \qquad \qquad \tau_{HV}\dot{V}_{HV} = V_{HV}^{*} - V_{HV} - m(i_{HV} - i_{set})$$

For $m_1=m_2=\dots=m_n$, one obtains

$$\frac{1}{SOC_1^{\rho}}i_1 = \frac{1}{SOC_2^{\rho}}i_2 = \cdots = \frac{1}{SOC_n^{\rho}}i_n$$

which can guarantee load power sharing proportional to the BESS instantaneous state of charge.





Suppressing circulating currents

E.g. for two BESS at the LV bus, one has the circulating current i_{cc} as

$$i_{cc} = i_1 - \frac{m_2}{SOC_2^{\rho}} \left(\frac{m_1}{SOC_1^{\rho}}\right)^{-1} i_2 \qquad \longrightarrow \qquad i_{cc} \to 0$$

Voltage reference selection to avoid voltage collapse

 V^* selection is made satisfying an inequality of the form

 $V^* > \phi(P)$



Consider the input duty-ratio defined as

$$u_{j} = 1 - \frac{U_{j} - E_{max,j}sin(\sigma_{j}) + r_{v,j}i_{L,j}}{V_{j}}$$
$$\dot{\sigma}_{j} = \frac{k_{I,j}}{r_{v,j}}f(i_{L,j}, V_{j})cos(\sigma_{j})$$

where f is a smooth functions that incorporates the adaptive droop control, or the current control for HV interconnection

$$f(i_{L,j}, V_j) = \begin{cases} V^* - V_{LV} - \frac{m_i}{SOC_i^{\rho}} i_i, & \forall i \in \{1, \dots, n\} \\ V_{HV}^* - V_{HV} - m(i_{HV} - i_{set}) \end{cases}$$

Assumption. The present approach considers at least one converter-interfaced source connected to the low-voltage bus to stabilise the bus voltage.



By replacing the control input u_j into the current dynamics, one obtains the closed-loop current dynamics

$$L\frac{di_{L,j}}{dt} = E_{max,j}sin(\sigma_j) - (r_{s,j} + r_{v,j})i_{L,j}$$

At steady state, there is

$$i_{L,j} \approx \frac{E_{max,j}sin(\sigma_j)}{r_{v,j}}$$

Proposition 2. The solution $i_{L,j}(t)$ with the initial condition $i_{L,j}(0) \leq \frac{E_{max,j}}{r_{v,j}}$ is uniformly ultimately bounded, i.e. $|i_{L,j}(t)| \leq i_{L,j}^{max}$, $\forall t > 0$, with the maximum current given as $i_{L,j}^{max} = \frac{E_{max,j}}{r_{v,j}}$.

Proof. Consider the following continuously differentiable Lyaponov function candidate

$$W_j = \frac{1}{2} L_j i_{L,j}$$

By taking the time derivative

$$\dot{W}_{j} = -(r_{s,j} + r_{v,j})i_{L,j}^{2} + E_{max,j}sin(\sigma_{j})i_{L,j}$$



By taking the time derivative

$$\dot{W}_{j} \leq -(r_{s,j} + r_{v,j})i_{L,j}^{2} + |E_{max,j}sin(\sigma_{j})||i_{L,j}|$$

Given $\sigma_j \in \left[-\frac{\pi}{2}, \frac{\pi}{2}\right]$ from the nonlinear sl-PID design, one has

$$\dot{W}_{j} \leq -(r_{s,j}+r_{v,j})i_{L,j}^{2}+E_{max,j}|i_{L,j}|,$$

which implies that

$$\dot{W}_{j} \leq -r_{\mathrm{s},j} i_{L,j}^{2}, \qquad \forall \left| i_{L,j} \right| \geq \frac{E_{max,j}}{r_{v,j}}$$

The solution is uniformly ultimately bounded. Thus, if initially $i_{L,j}(0) \leq \frac{E_{max,j}}{r_{v,j}}$, then it holds that

$$i_{L,j}(t) \leq \underbrace{\frac{E_{max,j}}{r_{v,j}}}_{i_{L,j}^{max}} \forall t > 0$$

Simulation results





Simulation results







Conclusions

The controller proposed can achieve the following:

- ✓ Load power sharing among BESS
- ✓ Voltage regulation near reference
- ✓ Input/interconnection current limitation without knowledge of the system parameters and additional protection circuits such as limiters and/or saturators.



